

---SDRAM DATA BUS

----- DMA BUS

JTAG

FLASH MEMORY/ CFC/SSF

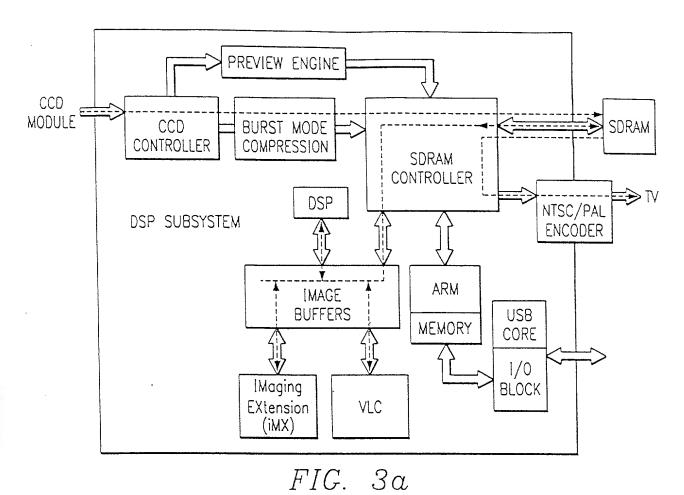
182

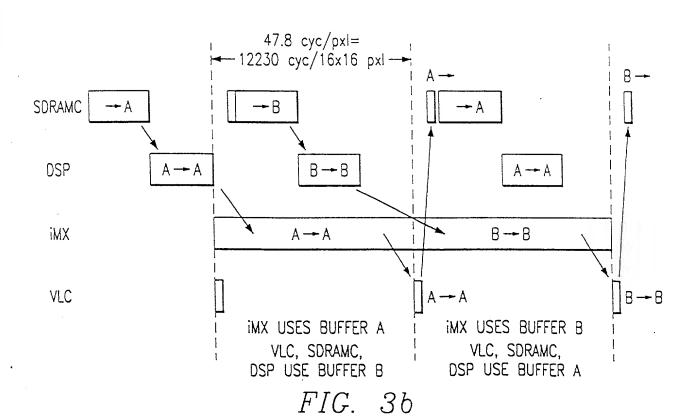
FIG. 1b

ENGINE

EXTENSION

I I'M Will from well I'M





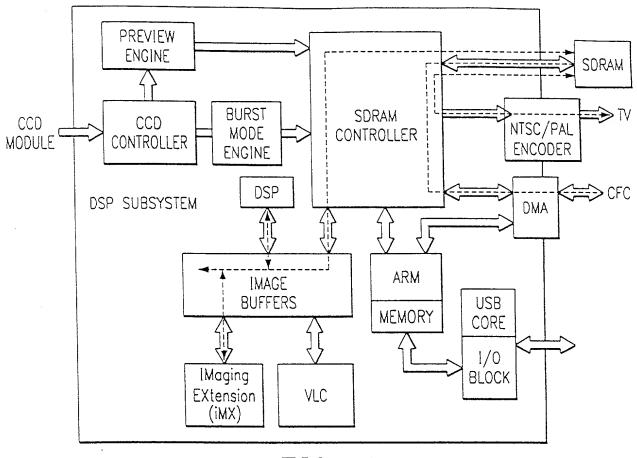
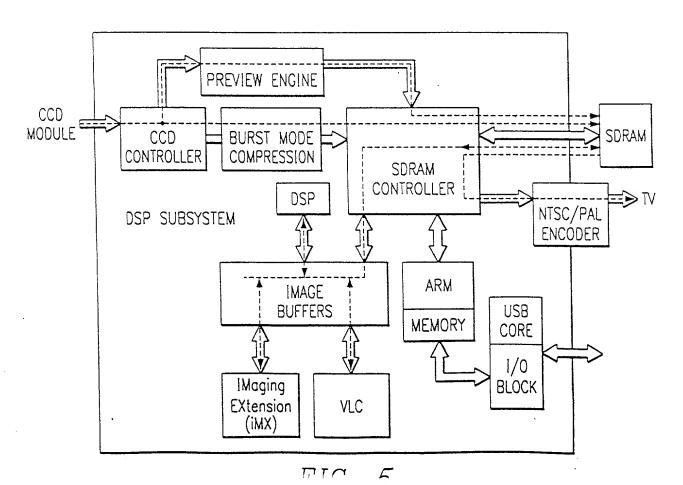
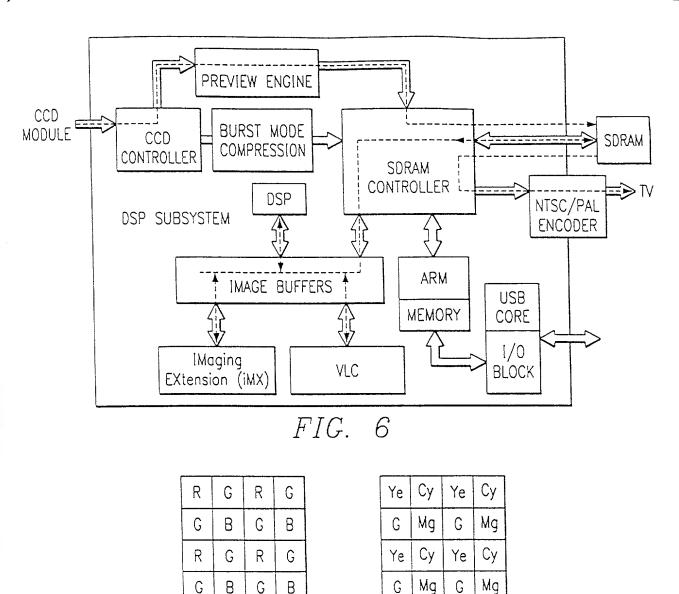
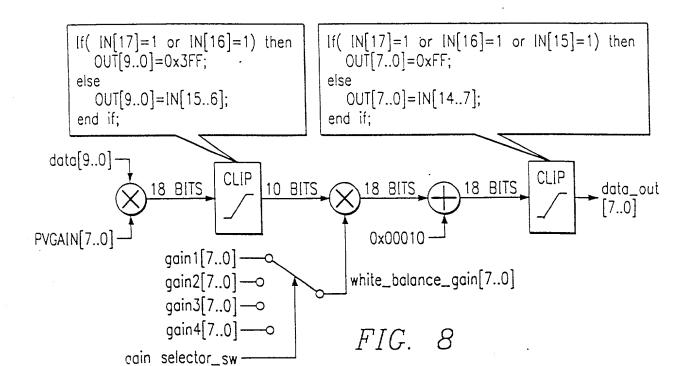


FIG. 4







G

FIG.

 $7\alpha$ 

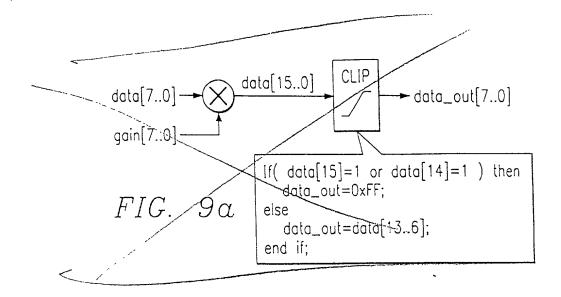
Mg

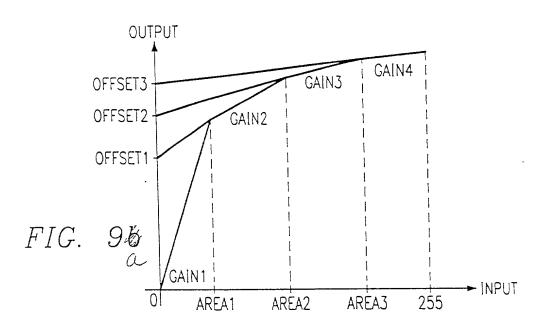
FIG.

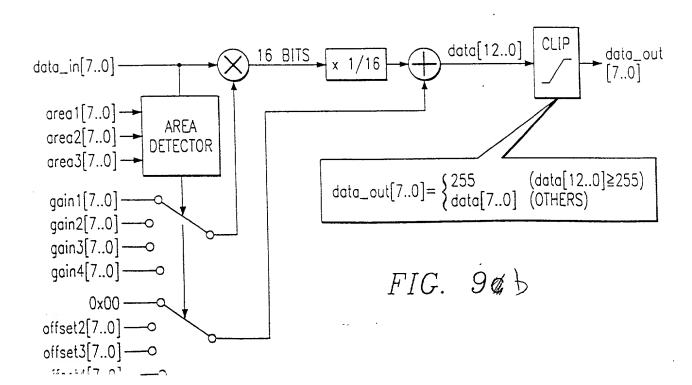
Mq

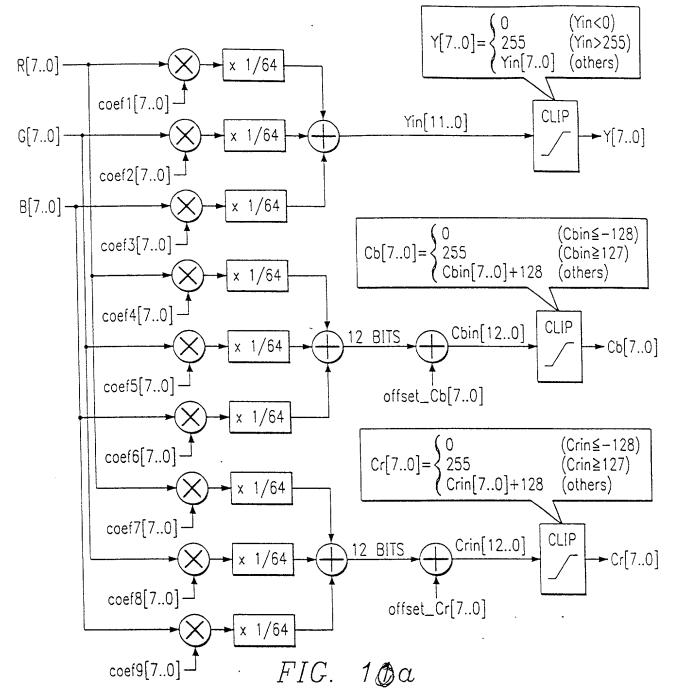
7b

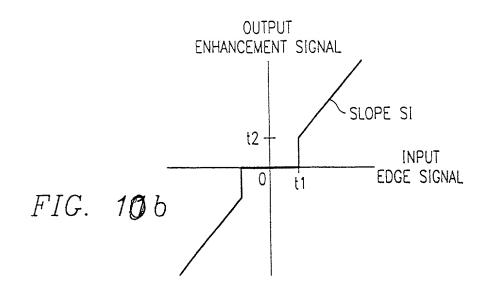
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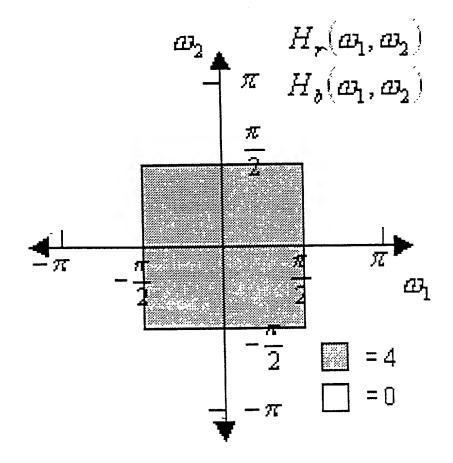
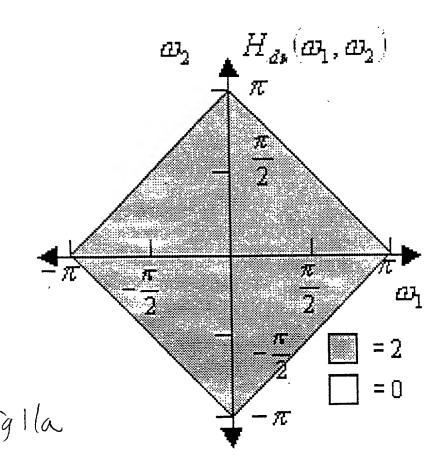


Fig.11b

(a) Ideal red/blue interpolation filter.



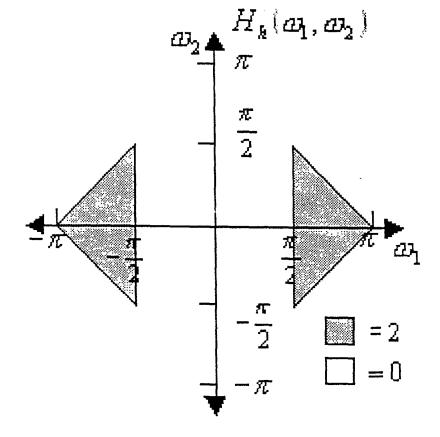


Fig. 11c

(a) Ideal horizontal high-pass filter.

